

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO.
09/625643

CONT/PRIOR
F

CLASS

SUBCLASS

ART UNIT

EXAMINER

Noriaki Hiraga

APPLICANTS

TITLE

Semiconductor integrated circuit device with enhanced resistance to electrostatic breakdown

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION											

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drawn	Figs. Drawn	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	

WARNING: The information disclosed herein may be restricted; Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181, and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form **PTO-438A**
(Rev. 6/99)

FILED WITH: ☒ DISK (CRF) ☒ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

(FACE)